



EV372458496

Inventor: **Fernando Gonzalez et al.**
Title: **Methods of Forming Semiconductor Circuitry**
Assignee: **Micron Technology, Inc.**
Serial No.: **09/989,931**
Filed: **November 21, 2001 [RCE Filed Herewith]**

INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the references listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Date:

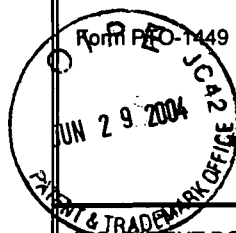
6/29/04

Attorney:

[Signature]

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EV372458496



U.S. DEPARTMENT OF COMMERCE
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ATTY. DOCKET NO.
M122-1801

SERIAL NO.
09/989,931

LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)

APPLICANT:
Fernando Gonzalez et al.

FILING DATE
Nov. 21, 2001 [RCE filed herewith]

GROUP
2815

U.S. PATENT DOCUMENTS

*Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,653,714 B2	11/03	Matsuno et al.			
	AB	4,903,108	02/90	Young et al.			
	AC	6,566,210 B2	05/03	Ajmera et al.			
	AD	5,650,343	07/97	Luning et al.			
	AE	6,306,691 B1	10/01	Koh			
	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AJ							
	AK							
	AL							

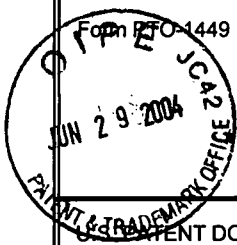
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

	AM		Lee, W. et al., "Investigation of Poly-Si _{1-x} Ge _x for Dual-Gate CMOS Technology", IEEE Electron Device Letters, Vol. 19, No. 7, July 1998, pp. 247-249.
	AN		Wolf, S., "Silicon Processing for the VLSI Era: Vol. 1: Process Technology", 1986 Lattice Press, pp. 191-195.
	AO		

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Form PTO-1449

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U.S. PATENT DOCUMENTS

*Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	2001/0045604 A1	11/29/01	Oda et al.			
	AB	2001/0008292 A1	07/19/01	Leobandung et al.			
	AC	2002/ 0019105 A1	02/14/02	Hattori et al.			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

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